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APPLICATION NO.	FILING DATE	RADE A PIRST NA	DEA PIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/491,900	01/27/2000	M . J	M. Jason Welch		10991989-1	9214	
TITLE OF INVENTION: METHOD OF INTEGRATED CIRCUIT CONSTRUCTION WITH PORT ALIGNMENT AND TIMING SIGNAL BUFFERING WITHIN COMMON AREA							
APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PL	BLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1330		\$0	\$1330	02/24/2004	
EXAMINER		ART UNIT		ASS-SUBCLASS]		
NGUYEN, DILINH P		2814	2814 257-298000				
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Please check the appropriate	assignee category or catego	ries (will not be printed on t	the patent);	🗆 individual 🛚 🛣	corporation or other private g	roup entity	
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